Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	4	huang-cheng-yen.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 08:59
S3	7601	access ADJ transistor$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 13:15
S4	7448	("bit line" bit-line) ADJ pair	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 13:15
S5	1122	latch$3 ADJ node	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 13:15
S6	1669	flush$3 ADJ line	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 13:16
S7	72816	select$3 ADJ (unit device)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 13:31
S8	42262	cach$3 ADJ memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 13:17
S9	1	S3 S5 S7 S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 13:17

S10	121	S3 S5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 13:18
S11	72	S8 S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 13:43
S12	147109	select$3 ADJ (unit device circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 13:31
S13	41	S3 S5 S12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 13:32
S14	28	S12 S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 13:53
S15	1911	(SRAM "static random access memory") AND S7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 13:46
S16	23	S15 S5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 13:45
S17	8	S15 S5 S3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 13:45

S18	23	(SRAM "static random access memory") AND S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:00
S21	35	(flush$3 ADJ line) AND ("OR" ADJ gate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 14:43
S22	221	("parity bit" "control field") AND ("tag memory")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 14:04
S23	332	711/135.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 13:22
S25	51	("OR" ADJ gate) WITH flush	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:01
S26	168	flip-flop SAME driv$3 ADJ buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:36
S27	68	flip-flop WITH driv$3 ADJ buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:22
S28	3	"D flip-flop" SAME driv$3 ADJ buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:38

S29	2	"DFF" SAME driv$3 ADJ buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:37
S30	8	"D-type flip-flop" SAME driv$3 ADJ buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:38
S31	1089	365/218.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/06 14:14
S32	7611	access ADJ transistor$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/06 14:15
S33	17	S31 AND S32	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/06 14:15
S35	0	("S3S6("OR"adjgate)").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/08 13:03
S36	7965	access ADJ transistor$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 13:03
S37	1704	flush$3 ADJ line	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 13:03

S38	1	S36 and S37 and ("or" adj gate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 13:06
S39	3	S36 and S37	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 13:08
S40	212	S37 and latch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 13:24
S41	36	S37 and ("or" adj gate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 13:08
S42	2209	365/154.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 13:21
S43	351	711/135.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 13:22
S44	351	S43	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 13:22
S45	1093	365/218.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 13:22

S46	1093	S45	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 13:22
S47	107	S37 same latch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 13:30
S49	183001	(cross adj coupl$3) "6T" SRAM	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 13:35
S50	1594	S49 and cache and flush$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 13:33
S51	7	S50 and (access adj transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 13:33
S52	126908	(cross adj coupl$3) "6T"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 13:36
S53	66	S52 and cache and flush	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 13:36
S54	20	("4567578").URPN.	USPAT	OR	ON	2006/02/08 14:12
S55	10	("3845474" | "3979726" | "3980899" | "4159540" | "4172291" | "4236207" | "4247918" | "4360901" | "4439843" | "4489404").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/08 14:25

S59	25	SRAM near10 cach$3 near10 (clear$3 or initializ$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 09:00